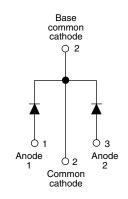


Vishay Semiconductors

Schottky Rectifier, 2 x 30 A

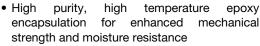


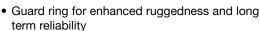


PRODUCT SUMMARY					
Package	TO-247AC				
I _{F(AV)}	2 x 30 A				
V_R	150 V				
V _F at I _F	0.67 V				
I _{RM} max.	25 mA at 125 °C				
T _J max.	175 °C				
Diode variation	Common cathode				
E _{AS}	0.5 mJ				

FEATURES

- 175 °C T_J operation
- Low forward voltage drop
- High frequency operation





- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)



FREE

DESCRIPTION

The VS-60CPQ150... center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL CHARACTERISTICS VALUES UNITS							
I _{F(AV)}	Rectangular waveform	60	A				
V _{RRM}		150	V				
I _{FSM}	t _p = 5 µs sine	2300	A				
V _F	30 Apk, T _J = 125 °C (per leg)	0.67	V				
T _J	Range	- 55 to 175	°C				

VOLTAGE RATINGS							
PARAMETER	SYMBOL	VS-60CPQ150PbF	VS-60CPQ150-N3	UNITS			
Maximum DC reverse voltage	V _R	150	150	V			
Maximum working peak reverse voltage	V_{RWM}	150	150	V			

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYN	/IBOL	TEST COND	ITIONS	VALUES	UNITS	
Maximum average forward current	per leg		50 % duty cycle at T _C = 151 °C, rectangular waveform		30		
	device	(AV)			60	Α	
Maximum peak one cycle non-repetitive surge current per leg See fig. 7			5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	2300	A	
		I _{FSM}	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	510		
Non-repetitive avalanche energy per leg E_{AS} $T_J = 25$ °C, $I_{AS} = 1$ A, L = 1 mH		0.5	mJ				
Repetitive avalanche current per leg	I,	AR		Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5 \text{ x } V_R$ typical		Α	

Revision: 31-Aug-11 Document Number: 94238



VS-60CPQ150PbF, VS-60CPQ150-N3

Vishay Semiconductors

ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	NDITIONS	TYP.	MAX.	UNITS	
Maximum forward voltage drop per leg See fig. 1		30 A	- T _J = 25 °C	0.80	0.83	V	
	V _{FM} ⁽¹⁾	60 A		0.93	0.99		
		30 A	T 105 00	0.64	0.67		
		60 A	T _J = 125 °C	0.74	0.77		
Maximum reverse leakage current per leg	I _{RM}	T _J = 25 °C	V _R = Rated V _R	10	100	μA	
See fig. 2		T _J = 125 °C	v _R = nateu v _R	12	25	mA	
Typical junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25 °C		-	820	pF	
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		-	7.5	nΗ	
Maximum voltage rate of change	dV/dt	Rated V _R		-	10 000	V/µs	

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range)	T _J , T _{Stg}		- 55 to 175	°C	
Maximum thermal resistance, junction to case per leg		В	DC operation See fig. 4	0.8		
Maximum thermal resistance, junction to case per package		R _{thJC}	DC operation	0.4	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.25		
Anarovimoto weight				6	g	
Approximate weight				0.21	OZ.	
Maunting torque	minimum			6 (5)	kgf · cm	
Mounting torque -	maximum			12 (10)	(lbf ⋅ in)	
Marking device			Case style TO-247AC (JEDEC)	60CPQ150		

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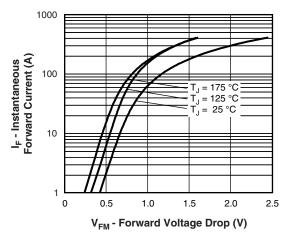


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

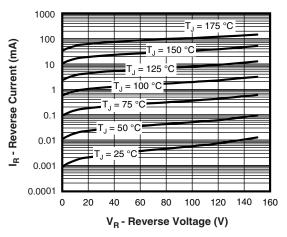


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

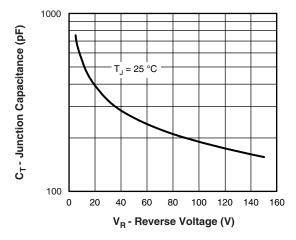


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

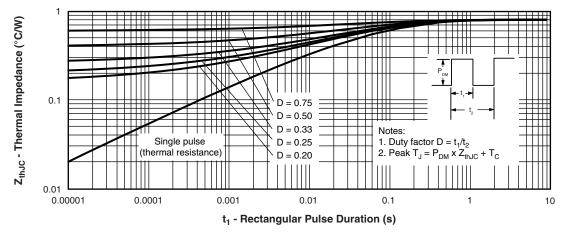


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

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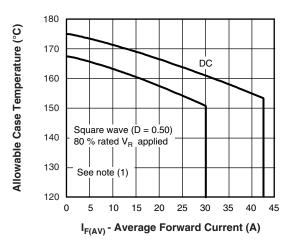


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

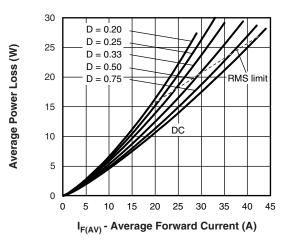


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

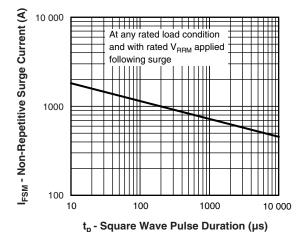


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

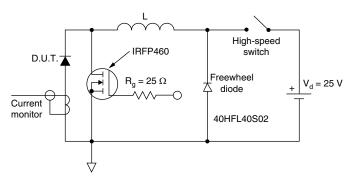


Fig. 8 - Unclamped Inductive Test Circuit

Note

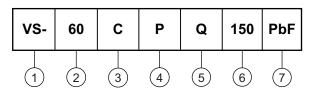
Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80 \%$ rated V_R

VS-60CPQ150PbF, VS-60CPQ150-N3

Vishay Semiconductors

ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (60 = 60 A)

3 - Circuit configuration:

C = Common cathode

4 - Package:

P = TO-247

5 - Schottky "Q" series

6 - Voltage code (150 = 150 V)

7 - Environmental digit

• PbF = Lead (Pb)-free and RoHS compliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

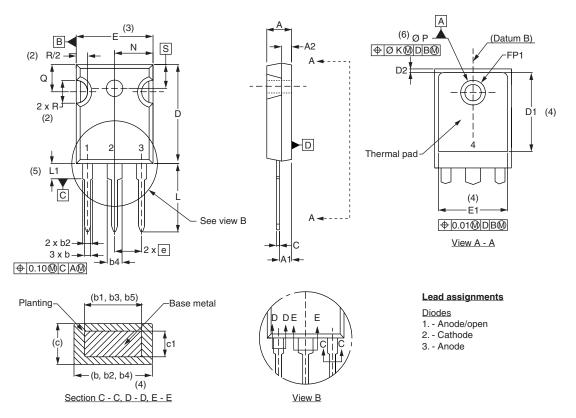
ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-60CPQ150PbF	25	500	Antistatic plastic tube				
VS-60CPQ150-N3	25	500	Antistatic plastic tube				

LINKS TO RELATED DOCUMENTS						
Dimensions <u>www.vishay.com/doc?95223</u>						
Part marking information	TO-247AC PbF	www.vishay.com/doc?95226				
Part marking information	TO-247AC -N3	www.vishay.com/doc?95007				



Vishay Semiconductors

DIMENSIONS in millimeters and inches



SYMBOL	MILLIN	IETERS	INCHES		NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.37	0.065	0.094	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
С	0.38	0.86	0.015	0.034	
c1	0.38	0.76	0.015	0.030	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.30	0.020	0.051	
E	15.29	15.87	0.602	0.625	3
E1	13.72	-	0.540	-	
е	5.46	BSC	0.215	BSC	
FK	2.	54	0.0	010	
L	14.20	16.10	0.559	0.634	
L1	3.71	4.29	0.146	0.169	
N	7.62	BSC	0	.3	
ΦР	3.56	3.66	0.14	0.144	
ФР1	1	6.98	-	0.275	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	1.78	0.216	
S	5.51	BSC	0.217	'BSC	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC outline TO-247 with exception of dimension c



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